

PATENT NUMBER

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MT O.I.P.E. SCANNED <i>ok</i> on <i>AG</i>	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/996788	F	438	700	1765	chen, K

Naokatsu Ikegami

Method for manufacturing a semiconductor device

[illegible]

**TERMINAL
DISCLAIMER**

Flgs. Drwg.	Print Flg
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Total Claims	Print Claim for O.G.
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☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____

(Assistant Examiner) (Date)

Amount Due	Date Paid
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(Legal Instruments Examiner) (Date)

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